ASA-1062



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE In re Patent Application of

T. SASAKI et al.

Serial No. 10/073,312

Group Art Unit: 2825

Filed:

February 13, 2002

Examiner: M. Dimyan

For: DESIGN METHOD AND SYSTEM FOR

SEMICONDUCTOR INTEGRATED CIRCUITS

PETITION FOR EXTENSION OF TIME

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

February 25, 2005

Sir:

It is respectfully requested that a Three-month Extension of Time, to and including February 25, 2005, be granted in which to respond to the Office Action dated August 25, 2004, in the above-identified application.

Our Credit Card Payment Form in the amount of \$1,020.00 is attached in payment of the appropriate fee.

The Commissioner is hereby authorized to charge any additional payment due, or to credit any overpayment, to Deposit Account No. 50-1417.

Respectfully submitted,

Registration No. 32,846

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Date: February 25, 2005

02/28/2005 SSITHIB1 00000126 10073312 02 FC:1253

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